Doctoral Examination of Mr. Mauro Martin Letras Luna

Nomination of the Examination Council and Invitation to the Oral Examination

Supervising Institute: Hardware-Software-Co-Design

Dear Sir or Madam,

As a part of his doctoral examination, Mauro Martin Letras Luna will give a half-hour lecture on the topic: "Techniques for Efficient Performance Analysis and Memory Optimization in Mapping Dataflow Models of Computation onto Embedded Systems".

Date and Time: 02 August 2024, 3:00 PM
Venue: E-Studio, 2.038, Martensstraße 1, 91058 Erlangen

The lecture will be followed by a 15 minutes discussion. Both the lecture and discussion are open to the public. A viva voce, closed to the public and lasting 45 minutes, will then take place.

The examination council consists of the following professors:

Prof. Dr. Wanka Chairperson
Prof. Dr.-Ing. Teich 1. Reviewer
Prof. Dr. Bhattacharyya (Attends by video conferencing) 2. Reviewer
Prof. Dr.-Ing. Weigel Further Examiner

The examination will only take place if the entire examination council is present at the exam and if there are no objections during the two-week period of display.

Kind regards,

sgnd. Graichen

Professor Dr. -Ing. Knut Graichen
Dean of Study